

⁹
~~10~~. A process for connecting two sections of a bus, by transmitting data from one section of the bus to the other section of the bus, comprising the steps of
-detecting an address sent from one section of the bus to the other section of the bus;
-remapping the address before transmitting it to the other section of the bus.

¹⁰
~~11~~. A process according to claim ~~10~~⁹, wherein the step of detecting comprises detecting a START condition preceding an address.

¹¹
~~12~~. A process according to claim ~~11~~¹⁰, wherein the step of remapping comprises changing at least one bit of an address, preferably one bit of a fixed part of an address.

¹²
~~13~~. A computer system including a bus, having at least an address remapper defining two sections in the bus, each section comprising at least one station having a physical address, wherein a station on one section of the bus is assigned a dummy address for being addressed by a station on the other section, the address remapper remapping a dummy address from the other section into a physical address to the one section.

¹³
~~14~~. A computer system according to claim ~~13~~¹², wherein the format of a physical address comprises a fixed part and a setable part, and wherein the dummy address is obtained by changing at least one bit of the fixed part of the physical address.

¹⁴
~~13~~. A computer system according to claim ¹²~~13~~, wherein the dummy addresses for stations out of a given section are different from the physical addresses for stations in said given section.

¹⁵
~~16~~. A bus having at least an address remapper defining at least two sections of the bus, each section of the bus having at least one station having a physical address and wherein each station in each section of the bus is assigned a dummy address for use when being addressed by a station on the other bus section, the address remapper remapping dummy addresses on either section of the bus into a corresponding physical address on the other section of the bus.

¹⁶
~~17~~. The bus of claim ¹⁵~~16~~ wherein a station on one portion of the bus and another station on the other portion of the bus share a common physical address.

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¹⁸
~~18~~. (New) A bus, having at least an address remapper defining two sections in the bus, each section including at least one station having a physical address, wherein each station on each section of the bus is assigned a dummy address by the remapper for the purpose of being addressed by a station on the other section, the address remapper transparently remapping each dummy address from one section into a physical address on the other section.

¹⁸
~~19~~. (New) A bus as claimed in claim ¹⁵~~16~~ wherein the address remapping is performed by changing at least one bit of the address of each station.